

Low Noise Pseudomorphic HEMT in a Surface Mount Plastic Package

Technical Data

ATF-33143

Features

- Low Noise Figure
- Excellent Uniformity in Product Specifications
- Low Cost Surface Mount Small Plastic Package SOT-343 (4 lead SC-70)
- Tape-and-Reel Packaging Option Available

Specifications

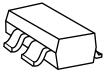
1.9 GHz; 4V, 80 mA (Typ.)

- 0.5 dB Noise Figure
- 15 dB Associated Gain
- 22 dBm Output Power at 1 dB Gain Compression
- 33.5 dBm Output 3rd Order Intercept

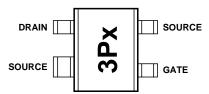
Applications

- Low Noise Amplifier and Driver Amplifier for Cellular/PCS Base Stations
- LNA for WLAN, WLL/RLL, LEO, and MMDS Applications
- General Purpose Discrete
 PHEMT for Other Ultra Low
 Noise Applications

Surface Mount Package SOT-343



Pin Connections and Package Marking



Note: Top View. Package marking provides orientation and identification.

"3P" = Device code

"x" = Date code character. A new character is assigned for each month, year.

Description

Hewlett Packard's ATF-33143 is a high dynamic range, low noise, PHEMT housed in a 4-lead SC-70 (SOT-343) surface mount plastic package.

Based on its featured performance, ATF-33143 is suitable for applications in cellular and PCS base stations, LEO systems, MMDS, and other systems requiring super low noise figure with good intercept in the 450 MHz to 10 GHz frequency range.

Other PHEMT devices in this family are the ATF-34143 and the ATF-35143. The typical specifications for these devices at 2 GHz are shown in the table below:

Part No.	Gate Width	Bias Point	NF (dB)	Ga (dB)	OIP3 (dBm)
ATF-33143	1600 μ	4 V, 80 mA	0.5	15.0	33.5
ATF-34143	800 μ	4 V, 60 mA	0.5	17.5	31.5
ATF-35143	400 μ	4V, 30 mA	0.4	18.0	28.0

Symbol	Parameter	Units	Absolute Maximum
V _{DS}	Drain - Source Voltage ^[2]	V	5.5
V _{GS}	Gate - Source Voltage ^[2]	V	-5
V _{GD}	Gate Drain Voltage ^[2]	V	-5
I _{DS}	Drain Current ^[2]	mA	I _{dss} ^[3]
P _{diss}	Total Power Dissipation ^[4]	mW	600
P _{in max}	RF Input Power	dBm	20
T _{CH}	Channel Temperature	°C	160
T _{STG}	Storage Temperature	°C	-65 to 160
θ_{jc}	Thermal Resistance ^[5]	°C/W	145

ATF-33143 Absolute Maximum Ratings^[1]

Notes:

- Operation of this device above any one of these parameters may cause permanent damage.
- 2. Assumes DC quiesent conditions. 3. $V_{GS} = 0 V$
- 4. Source lead temperature is 25°C. Derate 6 mW/°C for $T_I > 60°C$.
- Thermal resistance measured using 150°C Liquid Crystal Measurement method.

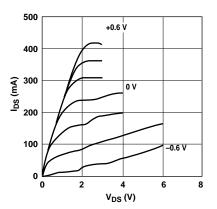
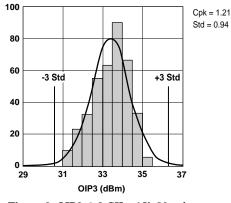
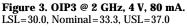


Figure 1. Typical I-V Curves^[6]. $(V_{GS} = -0.2 V \text{ per step})$





Notes:

Product Consistency Distribution Charts^[7, 8]

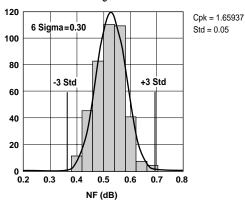


Figure 2. NF @ 2 GHz, 4 V, 80 mA. LSL=0.2, Nominal=0.53, USL=0.8

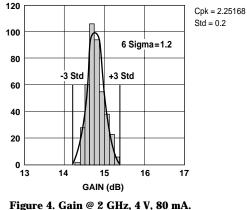


Figure 4. Gain @ 2 GHz, 4 V, 80 mA LSL=13.5, Nominal=14.8, USL=

- Distribution data sample size is 450 samples taken from 9 different wafers. Future wafers allocated to this product may have nominal values anywhere within the upper and lower spec limits.
- Measurements made on production test board. This circuit represents a trade-off between an optimal noise match and a realizeable match based on production test requirements. Circuit losses have been deembedded from actual measurements.

ATF-33143 DC Electrical Specifications

 $T_{\rm A}$ = 25°C, RF parameters measured in a test circuit for a typical device

Symbol	Parameter	s and Test Condit	ions	Units	Min.	Typ. ^[2]	Max.
I _{dss} ^[1]	Saturated Drain Curren	nt	$V_{DS} = 1.5 \text{ V}, V_{GS} = 0 \text{ V}$	mA	175	237	305
V _P ^[1]	Pinchoff Voltage	V _{DS}	= 1.5 V, I_{DS} = 10% of I_{dss}	V	-0.65	-0.5	-0.35
I _d	Quiescent Bias Curren	t	$V_{GS} = 0.42$ V, $V_{DS} = 4$ V	mA	—	100	_
g _m ^[1]	Transconductance		V_{DS} = 1.5 V, g_m = I_{dss}/V_P	mmho	360	440	_
I _{GDO}	Gate to Drain Leakage	Current	$V_{GD} = 5 V$	μA			1000
Igss	Gate Leakage Current		$V_{GD} = V_{GS} = -4 V$	μA	—	42	600
NF	Noise Figure	f = 2 GHz	$\label{eq:VDS} \begin{array}{l} V_{DS}=4 \ \text{V}, \ I_{DS}=80 \ \text{mA} \\ V_{DS}=4 \ \text{V}, \ I_{DS}=60 \ \text{mA} \end{array}$	dB		0.5 0.5	0.8
INI.	Noise Figure	f = 900 MHz	$\label{eq:VDS} \begin{array}{l} V_{DS} = 4 \ V \!\!\!, \ I_{DS} = 80 \ mA \\ V_{DS} = 4 \ V \!\!\!, \ I_{DS} = 60 \ mA \end{array}$	dB		0.4 0.4	
Ga	Associated Gain ^[3]	f = 2 GHz	$\label{eq:VDS} \begin{array}{l} V_{DS} = 4 \ V \!\!\!, \ I_{DS} = 80 \ mA \\ V_{DS} = 4 \ V \!\!\!, \ I_{DS} = 60 \ mA \end{array}$	dB	13.5	15 15	16.5
U _a	Associated Games	f = 900 MHz	$\label{eq:VDS} \begin{array}{l} V_{DS}=4 \ \text{V}, \ I_{DS}=80 \ \text{mA} \\ V_{DS}=4 \ \text{V}, \ I_{DS}=60 \ \text{mA} \end{array}$	dB		21 21	
OIP3	Output 3 rd Order	f = 2 GHz 5 dBm Pout/Tone		dBm	30	33.5 32	
	Intercept Point ^[3]	f = 900 MHz 5 dBm Pout/Tone		dBm		32.5 31	
		f = 2 GHz	25 25	dBm		22	
P _{1dB}	1 dB Compressed		$V_{\rm DS} = 4 \text{ V}, \text{ I}_{\rm DS} = 60 \text{ mA}$			21	
Tub	Compressed Power ^[3]	f = 900 MHz	20 20	dBm		21	
			$V_{\rm DS} = 4 \text{ V}, \text{ I}_{\rm DS} = 60 \text{ mA}$			20	

Notes:

1. Guaranteed at wafer probe level.

2. Typical value determined from a sample size of 450 parts from 9 wafers.

3. Measurements obtained using production test board described in Figure 5.

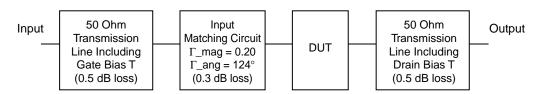
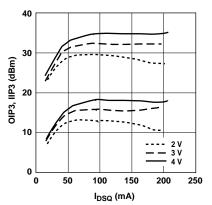
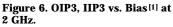


Figure 5. Block diagram of 2 GHz production test board used for Noise Figure, Associated Gain, P_{1dB} , and OIP3 measurements. This circuit represents a trade-off between an optimal noise match and a realizable match based on production test requirements. Circuit losses have been de-embedded from actual measurements.





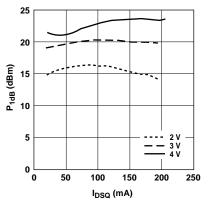
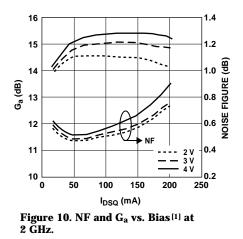


Figure 8. P_{1dB} vs. Bias^[1,2] at 2 GHz.



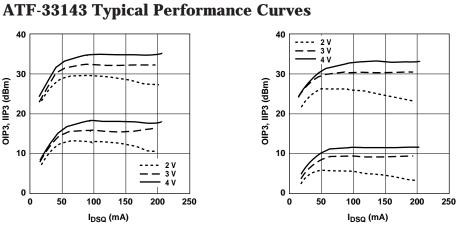


Figure 7. OIP3, IIP3 vs. Bias^[1] at 900 MHz.

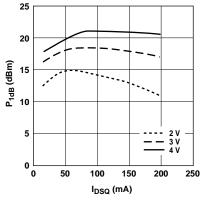
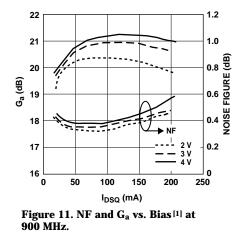


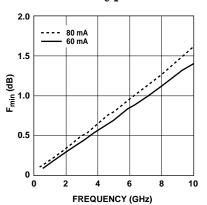
Figure 9. P_{1dB} vs. Bias^[1,2] Tuned for NF @ 4V, 80 mA at 900 MHz.



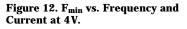
Notes:

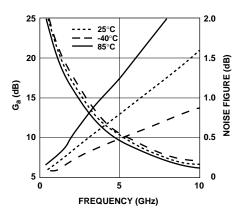
- 1. Measurements made on a fixed tuned production test board that was tuned for optimal gain match with reasonable noise figure at 4V 80 mA bias. This circuit represents a trade-off between optimal noise match, maximum gain match and a realizable match based on production test board requirements. Circuit losses have been de-embedded from actual measurements.
- 2. P_{1dB} measurements are performed with passive biasing. Quiescent drain current, I_{DSQ}, is set with zero RF drive applied. As P_{1dB} is approached, the drain current may increase or decrease depending on frequency and dc bias point. At lower values of I_{DSO} the device is running closer to class B as power output approaches P_{1dB} . This results in higher P_{1dB} and higher PAE (power added efficiency) when compared to a device that is driven by a constant current source as is typically done with active biasing. As an example, at a V_{DS} = 4 V and I_{DSQ} = 20 mA, I_d increases to 62 mA as a P_{1dB} of +19 dBm is approached.

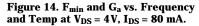
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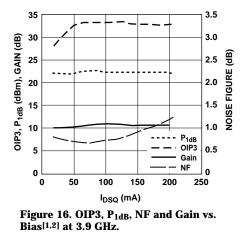


ATF-33143 Typical Performance Curves, continued









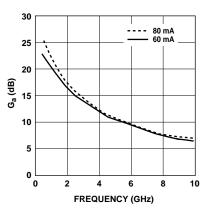
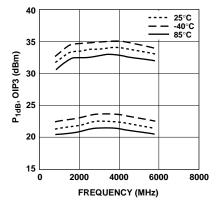
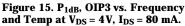
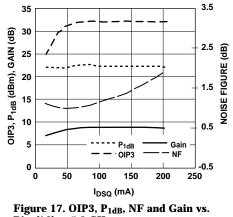


Figure 13. Associated Gain vs. Frequency and Current at 4V.



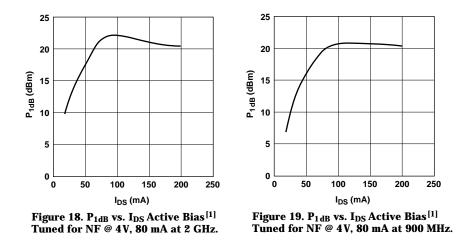




Bias [1,2] at 5.8 GHz.

Notes:

- 1. Measurements made on a fixed tuned test fixture that was tuned for noise figure at 4V 80 mA bias. This circuit represents a trade-off between optimal noise match, maximum gain match and a realizable match based on production test requirements. Circuit losses have been de-embedded from actual measurements.
- 2. P_{1dB} measurements are performed with passive biasing. Quiescent drain current, I_{DSQ}, is set with zero RF drive applied. As P_{1dB} is approached, the drain current may increase or decrease depending on frequency and dc bias point. At lower values of Idsq the device is running closer to class B as power output approaches P_{1dB} . This results in higher P_{1dB} and higher PAE (power added efficiency) when compared to a device that is driven by a constant current source as is typically done with active biasing. As an example, at a $V_{DS} = 4 V$ and I_{DSQ} = 20 mA, I_d increases to 62 mA as a P_{1dB} of +19 dBm is approached.



ATF-33143 Typical Performance Curves, continued

Note:

1. Measurements made on a fixed tuned test board that was tuned for optimal gain match with reasonable noise figure at 4V 80 mA bias. This circuit represents a trade-off between an optimal noise match, maximum gain match and a realizable match based on production test board requirements. Circuit losses have been de-embedded from actual measurements.

Freq (GHz)	P _{1dB} (dBm)	I _d (mA)	G _{1dB} (dB)	PAE _{1dB} (%)	P _{3dB} (dBm)	I _d (mA)	PAE _{3dB} (%)	Г Out_mag (Mag.)	Г Out_ang (°)
0.9	20.7	89	23.2	33	23.2	102	51	0.39	160
1.5	21.2	91	20.7	36	23.8	116	51	0.43	165
1.8	21.1	80	19.2	40	23.0	94	52	0.43	170
2.0	21.6	81	18.1	44	23.2	89	57	0.42	174
4.0	23.0	97	11.9	48	24.6	135	48	0.40	-150
6.0	24.0	130	5.9	36	25.2	136	36	0.37	-124

ATF-33143 Power Parameters Tuned for Max P_{1dB} , $V_{DS} = 4 V$, $I_{DSQ} = 80 mA$

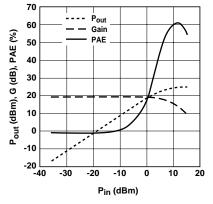


Figure 20. Swept Power Tuned for Max P_{1dB} V_{DS} = 4V, I_{DSQ} = 80 mA, 2 GHz.

Notes:

- 1. Measurements made on ATN LP1 power load pull system.
- 2. P_{1dB} measurements are performed with passive biasing. Quicescent drain current, I_{DSQ} , is set with zero RF drive applied. As P_{1dB} is approached, the drain current may increase or decrease depending on frequency and dc bias point. At lower values of I_{DSQ} the device is running closer to class B as power output approaches P_{1dB} . This results in higher P_{1dB} and higher PAE (power added efficiency) when compared to a device that is driven by a constant current source as is typically done with active biasing. As an example, at a $V_{DS} = 4$ V and $I_{DSQ} = 20$ mA, I_d increases to 62 mA as a P_{1dB} of +19 dBm is approached.
- 3. PAE (%) = $((P_{out} P_{in}) / P_{dc}) \tilde{X} 100$
- 4. Gamma out is the reflection coefficient of the matching circuit presented to the output of the device.

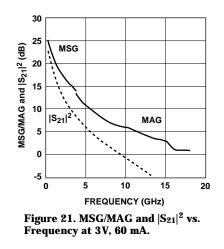
Freq.		5 ₁₁		S ₂₁			S ₁₂		5	522	MSG/MAG	K
(GHz)	Mag.	Ang.	dB	Mag.	Ang.	dB	Mag.	Ang.	Mag.	Ang.	(dB)	
0.5	0.87	-82.93	22.77	13.75	129.71	-27.96	0.040	52.00	0.34	-139.11	25.28	0.26
0.8	0.80	-110.10	20.74	10.89	114.34	-26.20	0.049	43.35	0.40	-155.74	23.14	0.43
1.0	0.77	-129.25	19.01	8.92	103.10	-25.19	0.055	38.29	0.43	-165.96	22.04	0.51
1.5	0.74	-155.43	16.18	6.44	86.47	-23.74	0.065	32.41	0.46	178.89	19.90	0.67
1.8	0.74	-165.25	15.03	5.64	79.74	-23.10	0.070	30.36	0.47	172.98	18.91	0.74
2.0	0.73	-173.80	14.01	5.02	73.56	-22.62	0.074	28.53	0.48	167.68	18.25	0.79
2.5	0.73	171.48	12.27	4.11	62.30	-21.62	0.083	24.60	0.48	158.12	16.89	0.87
3.0	0.73	158.20	10.83	3.48	51.85	-20.82	0.091	20.35	0.49	149.88	15.72	0.93
4.0	0.73	134.86	8.47	2.65	32.58	-19.41	0.107	10.66	0.50	134.51	13.18	1.01
5.0	0.75	116.04	6.55	2.13	14.46	-18.42	0.120	-0.22	0.52	117.92	10.99	1.05
6.0	0.77	98.78	4.95	1.77	-3.17	-17.65	0.131	-12.38	0.54	101.20	9.47	1.08
7.0	0.78	80.95	3.48	1.49	-20.45	-17.14	0.139	-25.01	0.57	85.41	8.19	1.12
8.0	0.80	64.69	2.12	1.28	-36.99	-16.77	0.145	-37.16	0.59	69.82	7.17	1.13
9.0	0.82	49.72	0.88	1.11	-53.24	-16.54	0.149	-49.72	0.62	53.61	6.37	1.14
10.0	0.85	34.73	-0.18	0.98	-69.73	-16.25	0.154	-62.87	0.64	38.34	5.96	1.12
11.0	0.86	20.74	-1.44	0.85	-85.63	-16.42	0.151	-75.81	0.67	23.66	5.20	1.14
12.0	0.87	6.71	-2.67	0.74	-100.73	-16.59	0.148	-88.33	0.70	9.29	4.53	1.16
13.0	0.88	-8.29	-3.97	0.63	-115.32	-16.95	0.142	-100.45	0.73	-3.50	3.63	1.24
14.0	0.88	-19.46	-5.26	0.55	-127.89	-17.46	0.134	-110.25	0.77	-16.62	2.92	1.30
15.0	0.90	-28.00	-6.45	0.48	-140.34	-17.92	0.127	-120.07	0.79	-30.39	2.69	1.28
16.0	0.87	-40.30	-7.68	0.41	-153.80	-18.27	0.122	-131.08	0.81	-43.25	1.22	1.50
17.0	0.90	-48.76	-8.52	0.38	-164.56	-18.34	0.121	-141.30	0.82	-53.54	1.11	1.43
18.0	0.90	-60.79	-9.58	0.33	-175.28	-18.86	0.114	-149.36	0.85	-62.62	0.94	1.43

ATF-33143 Typical Scattering Parameters, $V_{DS} = 3 V$, $I_{DS} = 60 mA$

ATF-33143 Typical Noise Parameters

 $V_{DS} = 3 \text{ V}, I_{DS} = 60 \text{ mA}$

Freq.	F _{min}	Г	opt	R _{n/50}	Ga
GHz	dB	Mag.	Ang.	-	dB
0.5	0.10	0.78	33.20	0.088	22.86
0.9	0.14	0.73	51.92	0.084	20.00
1.0	0.15	0.71	56.60	0.082	19.54
1.5	0.21	0.64	80.00	0.069	17.27
1.8	0.25	0.60	94.04	0.059	16.30
2.0	0.28	0.50	101.20	0.052	15.69
2.5	0.34	0.46	127.60	0.037	14.47
3.0	0.42	0.43	151.20	0.028	13.37
4.0	0.55	0.42	-169.30	0.026	11.48
5.0	0.63	0.45	-138.30	0.053	9.94
6.0	0.78	0.51	-113.70	0.110	8.71
7.0	0.93	0.57	-93.50	0.210	7.71
8.0	1.09	0.63	-75.60	0.360	6.87
9.0	1.24	0.66	-57.90	0.560	6.14
10.0	1.39	0.66	-38.40	0.810	5.44



Notes:

1. F_{min} values at 2 GHz and higher are based on measurements while the F_{mins} below 2 GHz have been extrapolated. The F_{min} values are based on a set of 16 noise figure measurements made at 16 different impedances using an ATN NP5 test system. From these measurements a true F_{min} is calculated. Refer to the noise parameter application section for more information.

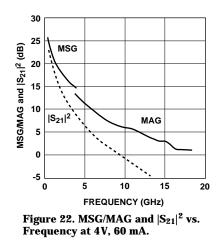
2. S and noise parameters are measured on a microstrip line made on 0.025 inch thick alumina carrier. The input reference plane is at the end of the gate lead. The output reference plane is at the end of the drain lead. The parameters include the effect of four plated through via holes connecting source landing pads on top of the test carrier to the microstrip ground plane on the bottom side of the carrier. Two 0.020 inch diameter via holes are placed within 0.010 inch from each source lead contact point, one via on each side of that point.

Freq.		5 ₁₁		S ₂₁			S ₁₂		5	522	MSG/MAG	K
(GHz)	Mag.	Ang.	dB	Mag.	Ang.	dB	Mag.	Ang.	Mag.	Ang.	(dB)	
0.5	0.86	-83.03	23.05	14.21	129.59	-27.74	0.041	51.28	0.31	-130.80	25.40	0.28
0.8	0.79	-110.26	21.02	11.25	114.02	-26.02	0.050	42.71	0.36	-150.74	23.26	0.43
1.0	0.76	-129.37	19.28	9.21	102.67	-25.04	0.056	37.63	0.40	-162.63	22.16	0.51
1.5	0.74	-155.36	16.43	6.63	85.91	-23.74	0.065	31.92	0.43	-179.67	20.08	0.67
1.8	0.73	-165.11	15.27	5.80	79.08	-23.10	0.070	29.92	0.43	173.75	19.04	0.74
2.0	0.72	-173.58	14.25	5.16	72.84	-22.62	0.074	28.22	0.44	167.95	18.43	0.79
2.5	0.72	171.73	12.51	4.22	61.42	-21.62	0.083	24.23	0.45	157.64	17.06	0.87
3.0	0.72	158.48	11.04	3.57	50.82	-20.82	0.091	19.98	0.46	148.72	15.93	0.94
4.0	0.73	135.46	8.65	2.71	31.36	-19.49	0.106	10.57	0.48	132.35	13.27	1.02
5.0	0.75	116.76	6.71	2.16	13.03	-18.49	0.119	-0.26	0.50	115.18	11.12	1.06
6.0	0.77	99.32	5.05	1.79	-4.89	-17.79	0.129	-12.23	0.53	98.25	9.63	1.09
7.0	0.79	81.87	3.52	1.50	-22.14	-17.39	0.135	-24.68	0.56	82.59	8.36	1.12
8.0	0.81	65.52	2.13	1.28	-38.79	-17.08	0.140	-36.54	0.59	67.59	7.34	1.14
9.0	0.83	50.35	0.79	1.10	-55.10	-16.89	0.143	-49.01	0.62	52.02	6.54	1.14
10.0	0.86	36.03	-0.34	0.96	-71.28	-16.71	0.146	-61.67	0.66	37.47	6.13	1.12
11.0	0.87	21.73	-1.62	0.83	-87.22	-16.89	0.143	-74.68	0.69	23.71	5.40	1.14
12.0	0.88	7.57	-2.96	0.71	-102.27	-17.20	0.138	-86.74	0.72	10.25	4.70	1.16
13.0	0.89	-6.35	-4.31	0.61	-116.25	-17.65	0.131	-97.88	0.75	-1.80	3.81	1.23
14.0	0.89	-17.98	-5.61	0.52	-128.88	-18.20	0.123	-107.54	0.79	-14.02	2.99	1.30
15.0	0.91	-26.46	-6.94	0.45	-141.19	-18.79	0.115	-117.22	0.81	-27.33	2.80	1.27
16.0	0.88	-37.13	-8.18	0.39	-154.02	-19.25	0.109	-126.70	0.83	-39.69	1.30	1.52
17.0	0.90	-47.04	-9.07	0.35	-165.19	-19.25	0.109	-137.71	0.84	-49.20	1.19	1.43
18.0	0.91	-58.62	-10.31	0.31	-175.61	-20.00	0.100	-145.35	0.87	-58.17	1.01	1.42

ATF-33143 Typical Scattering Parameters, $V_{DS} = 4 V$, $I_{DS} = 60 mA$

ATF-33143 Typical Noise Parameters $V_{\rm DS}$ = 4 V, $I_{\rm DS}$ = 60 mA

Freq.	F _{min}	Г	opt	R _{n/50}	Ga
GHz	dB	Mag.	Ang.	-	dB
0.5	0.11	0.72	38.40	0.070	22.90
0.9	0.15	0.66	57.60	0.066	20.50
1.0	0.16	0.64	62.40	0.064	20.00
1.5	0.22	0.57	86.40	0.053	18.00
1.8	0.26	0.52	100.80	0.045	16.85
2.0	0.28	0.49	110.40	0.040	16.24
2.5	0.35	0.43	133.00	0.030	14.74
3.0	0.42	0.40	153.60	0.027	13.41
4.0	0.56	0.38	-170.00	0.032	11.50
5.0	0.70	0.42	-139.60	0.060	10.00
6.0	0.84	0.49	-114.00	0.120	8.80
7.0	0.98	0.57	-92.40	0.220	7.80
8.0	1.12	0.64	-73.60	0.380	6.90
9.0	1.26	0.67	-56.80	0.590	6.20
10.0	1.40	0.65	-40.90	0.820	6.00



Notes:

1. F_{min} values at 2 GHz and higher are based on measurements while the F_{mins} below 2 GHz have been extrapolated. The F_{min} values are based on a set of 16 noise figure measurements made at 16 different impedances using an ATF NP5 test system. From these measurements a true F_{min} is calculated. Refer to the noise parameter application section for more information.

2. S and noise parameters are measured on a microstrip line made on 0.025 inch thick alumina carrier. The input reference plane is at the end of the gate lead. The output reference plane is at the end of the drain lead. The parameters include the effect of four plated through via holes connecting source landing pads on top of the test carrier to the microstrip ground plane on the bottom side of the carrier. Two 0.020 inch diameter via holes are placed within 0.010 inch from each source lead contact point, one via on each side of that point.

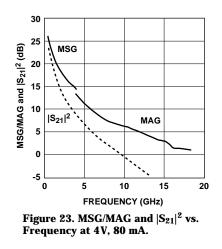
Freq.		5 11		S ₂₁			S ₁₂		S	522	MSG/MAG	K
(GHz)	Mag.	Ang.	dB	Mag.	Ang.	dB	Mag.	Ang.	Mag.	Ang.	(dB)	
0.5	0.85	-85.74	23.51	14.98	128.31	-28.87	0.036	52.29	0.32	-141.14	26.19	0.32
0.8	0.79	-112.96	21.40	11.75	112.92	-27.13	0.044	44.82	0.37	-158.04	23.95	0.49
1.0	0.76	-131.83	19.61	9.56	101.75	-26.02	0.050	40.46	0.41	-168.31	22.82	0.57
1.5	0.73	-157.31	16.72	6.86	85.32	-24.44	0.060	35.86	0.43	176.46	20.58	0.73
1.8	0.73	-166.86	15.55	5.99	78.63	-23.74	0.065	34.04	0.44	170.35	19.50	0.80
2.0	0.72	-175.15	14.52	5.32	72.53	-23.22	0.069	32.34	0.45	164.97	18.87	0.84
2.5	0.72	170.43	12.77	4.35	61.27	-22.05	0.079	28.72	0.46	155.23	17.41	0.91
3.0	0.72	157.40	11.30	3.67	50.83	-21.11	0.088	24.13	0.46	146.74	16.20	0.97
4.0	0.73	134.68	8.90	2.79	31.61	-19.58	0.105	14.29	0.48	130.93	13.20	1.03
5.0	0.75	116.19	6.95	2.23	13.46	-18.56	0.118	2.97	0.51	114.07	11.23	1.06
6.0	0.77	98.95	5.30	1.84	-4.28	-17.79	0.129	-9.43	0.54	97.33	9.78	1.08
7.0	0.79	81.59	3.76	1.54	-21.38	-17.33	0.136	-22.11	0.57	81.90	8.52	1.11
8.0	0.81	65.29	2.37	1.31	-37.89	-16.95	0.142	-34.35	0.60	67.06	7.52	1.12
9.0	0.83	50.19	1.03	1.13	-54.09	-16.83	0.144	-47.06	0.63	51.52	6.73	1.13
10.0	0.86	35.95	-0.10	0.99	-70.14	-16.65	0.147	-59.77	0.67	37.11	6.36	1.10
11.0	0.87	21.73	-1.39	0.85	-85.98	-16.77	0.145	-73.04	0.70	23.49	5.63	1.12
12.0	0.89	7.60	-2.73	0.73	-100.95	-17.08	0.140	-84.96	0.73	10.01	4.92	1.14
13.0	0.89	-6.28	-4.08	0.63	-114.87	-17.59	0.132	-96.51	0.76	-2.07	3.99	1.21
14.0	0.89	-18.01	-5.35	0.54	-127.35	-18.06	0.125	-106.16	0.80	-13.81	3.24	1.27
15.0	0.92	-26.22	-6.69	0.46	-139.50	-18.64	0.117	-115.94	0.81	-27.31	3.10	1.23
16.0	0.88	-37.03	-7.94	0.40	-152.37	-19.02	0.112	-125.59	0.83	-39.76	1.54	1.45
17.0	0.90	-46.87	-8.78	0.33	-163.68	-19.17	0.110	-136.99	0.84	-49.31	1.50	1.39
18.0	0.92	-58.50	-10.06	0.31	-173.94	-19.83	0.102	-144.32	0.87	-58.15	1.25	1.37

ATF-33143 Typical Scattering Parameters, $V_{DS} = 4 \text{ V}$, $I_{DS} = 80 \text{ mA}$

ATF-33143 Typical Noise Parameters

 $V_{DS} = 4 \text{ V}, I_{DS} = 80 \text{ mA}$

Freq.	F _{min}	Г	opt	R _{n/50}	Ga
GHz	dB	Mag.	Ang.	-	dB
0.5	0.11	0.70	41.30	0.066	25.00
0.9	0.16	0.64	61.14	0.063	21.92
1.0	0.17	0.63	66.10	0.063	21.51
1.5	0.25	0.55	90.90	0.055	18.58
1.8	0.30	0.51	105.78	0.047	17.43
2.0	0.33	0.48	115.70	0.041	16.66
2.5	0.41	0.42	138.70	0.032	15.10
3.0	0.49	0.39	159.70	0.030	13.74
4.0	0.65	0.38	-164.10	0.040	11.60
5.0	0.80	0.43	-134.30	0.080	10.10
6.0	0.96	0.50	-109.80	0.150	8.93
7.0	1.12	0.59	-89.30	0.280	7.87
8.0	1.27	0.66	-71.50	0.470	6.97
9.0	1.43	0.69	-55.20	0.720	6.46
10.0	1.59	0.65	-39.20	0.980	6.26



Notes:

- 1. F_{min} values at 2 GHz and higher are based on measurements while the F_{mins} below 2 GHz have been extrapolated. The F_{min} values are based on a set of 16 noise figure measurements made at 16 different impedances using an ATF NP5 test system. From these measurements a true F_{min} is calculated. Refer to the noise parameter application section for more information.
- 2. S and noise parameters are measured on a microstrip line made on 0.025 inch thick alumina carrier. The input reference plane is at the end of the gate lead. The output reference plane is at the end of the drain lead. The parameters include the effect of four plated through via holes connecting source landing pads on top of the test carrier to the microstrip ground plane on the bottom side of the carrier. Two 0.020 inch diameter via holes are placed within 0.010 inch from each source lead contact point, one via on each side of that point.

Noise Parameter Applications Information

F_{min} values at 2 GHz and higher are based on measurements while the F_{mins} below 2 GHz have been extrapolated. The F_{min} values are based on a set of 16 noise figure measurements made at 16 different impedances using an ATN NP5 test system. From these measurements, a true F_{min} is calculated. F_{min} represents the true minimum noise figure of the device when the device is presented with an impedance matching network that transforms the source impedance, typically 50 Ω , to an impedance represented by the reflection coefficient Γ_0 . The designer must design a matching network that will present Γ_0 to the device with minimal associated circuit losses. The noise figure of the completed amplifier is equal to the noise figure of the device plus the losses of the matching network preceding the device. The noise figure of the device is equal to F_{min} only when the device is

presented with Γ_{o} . If the reflection coefficient of the matching network is other than Γ_{o} , then the noise figure of the device will be greater than F_{min} based on the following equation.

NF = F_{min} + 4 R_n
$$\frac{|\Gamma_s - \Gamma_o|^2}{|1 + \Gamma_o|^2 (1 - \Gamma_o|^2)}$$

Where R_n/Z_o is the normalized noise resistance, Γ_0 is the optimum reflection coefficient required to produce F_{min} and Γ_s is the reflection coefficient of the source impedance actually presented to the device. The losses of the matching networks are non-zero and they will also add to the noise figure of the device creating a higher amplifier noise figure. The losses of the matching networks are related to the Q of the components and associated printed circuit board loss. Γ_0 is typically fairly low at higher frequencies and increases as frequency is lowered. Larger gate width devices will typically have a lower Γ_0 as compared to narrower gate width devices.

Typically for FETs, the higher Γ_0 usually infers that an impedance much higher than 50 Ω is required for the device to produce F_{min}. At VHF frequencies and even lower L Band frequencies, the required impedance can be in the vicinity of several thousand ohms. Matching to such a high impedance requires very hi-Q components in order to minimize circuit losses. As an example at 900 MHz, when airwwound coils (Q > 100)are used for matching networks. the loss can still be up to 0.25 dB which will add directly to the noise figure of the device. Using muiltilayer molded inductors with Qs in the 30 to 50 range results in additional loss over the airwound coil. Losses as high as 0.5 dB or greater add to the typical 0.15 dB F_{min} of the device creating an amplifier noise figure of nearly 0.65 dB. A discussion concerning calculated and measured circuit losses and their effect on amplifier noise figure is covered in **Hewlett-Packard Application** 1085.

ATF-33143 Die Model



Statz Model MESFETM1 NFET=yes PFET=no Vto=-0.95 Beta=0.48 Lambda=0.09 Alpha=4 B=0.8Tnom=27 Idstc= Vbi=0.7 Tau= Betatce= Delta1=0.2 Delta2= Gscap=3

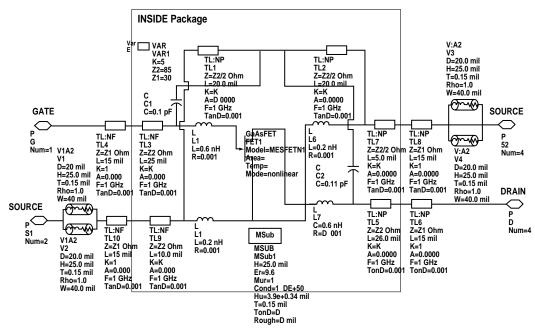
 $\begin{array}{c} Cgs\!=\!1.6 \ pF\\ Gdcap\!=\!3\\ Cgd\!=\!0.32 \ pF\\ Rgd\!=\\ Tqm\!=\\ Vmax\!=\\ Fc\!=\\ Rd\!=\!Rd\\ Rg\!=\!1\\ Rs\!=\!0.0625\\ Ld\!=\!0.00375 \ nH\\ Lg\!=\!0.00375 \ nH\\ Ls\!=\!0.00125 \ nH\\ Cds\!=\!0.08 \ pF\\ Crf\!=\!0.1 \end{array}$

 $\begin{array}{l} Rc{=}62.5\\ Gsfwd{=}1\\ Gsrev{=}0\\ Gdfwd{=}1\\ Is{=}1\ nA\\ Ir{=}1\ nA\\ Ira{=}1\ nA\\ Imax{=}0.1\\ Xti{=}\\ N{=}\\ Eg{=}\\ Vbr{=}\\ Vtorc{=}\\ Rin{=}\\ \end{array}$

Taumd1=no Fnc=1E6 R=0.17 C=0.2 P=0.65 wVgfwd= wBvgs= wBvgs= wBvds= wldsmax= wPmax= Al IParams=

This model can be used as a design tool. It has been tested on MDS for various specifications. However, for more precise and accurate design, please refer to the measured data in this data sheet. For future improvements Hewlett Packard reserves the right to change these models without prior notice.

ATF-33143 Model

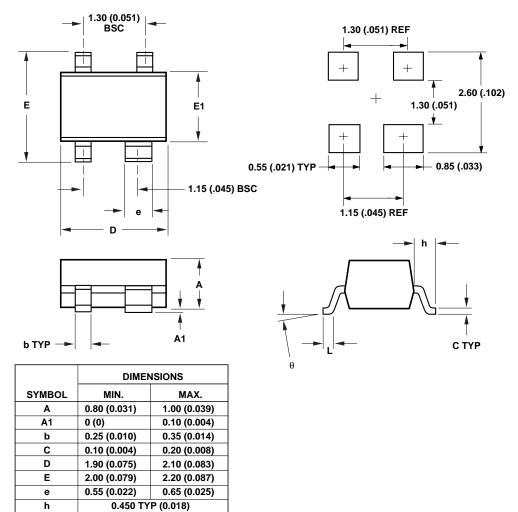


Part Number	Ordering	Information
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Deut Nouch au	No. of	Containen
Part Number	Devices	Container
ATF-33143-TR1	3000	7" Reel
ATF-33143-TR2	10000	13" Reel
ATF-33143-BLK	100	antistatic bag

Package Dimensions

Outline 43 (SOT-343/SC-70 4 lead)



0 DIMENSIONS ARE IN MILLIMETERS (INCHES)

1.15 (0.045)

0.10 (0.004)

1.35 (0.053)

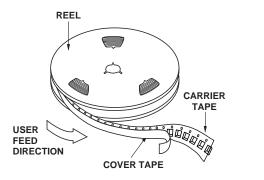
0.35 (0.014)

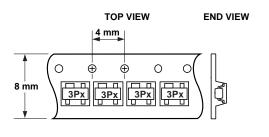
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E1

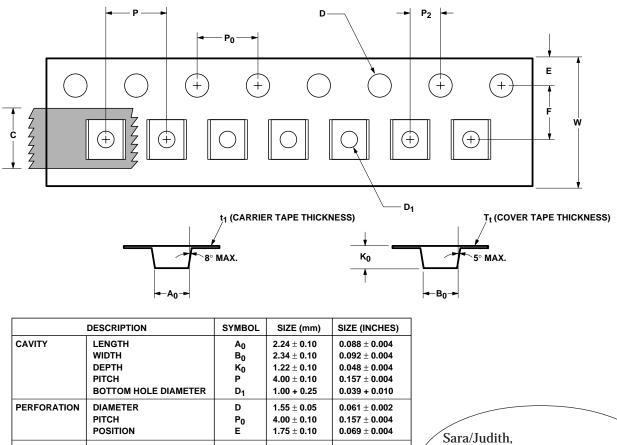
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Device Orientation





Tape Dimensions For Outline 4T



Input didn't show cover tape info. Should it be deleted? Pat

	BOTTOM HOLE DIAMETER	D ₁	1.00 + 0.25	0.039 + 0.010	
PERFORATION	DIAMETER	D	$\textbf{1.55} \pm \textbf{0.05}$	0.061 ± 0.002	
	PITCH	Po	$\textbf{4.00} \pm \textbf{0.10}$	0.157 ± 0.004	
	POSITION	E	$\textbf{1.75} \pm \textbf{0.10}$	$\textbf{0.069} \pm \textbf{0.004}$	
CARRIER TAPE	WIDTH	w	8.00 ± 0.30	0.315 ± 0.012	1 /
	THICKNESS	t ₁	$\textbf{0.255} \pm \textbf{0.013}$	0.010 ± 0.0005	
COVER TAPE	WIDTH	С	$\textbf{5.4} \pm \textbf{0.10}$	$\textbf{0.205} \pm \textbf{0.004}$	$\left \right $
	TAPE THICKNESS	Тt	$\textbf{0.062} \pm \textbf{0.001}$	0.0025 ± 0.00004	
DISTANCE	CAVITY TO PERFORATION (WIDTH DIRECTION)	F	3.50 ± 0.05	0.138 ± 0.002	1
	CAVITY TO PERFORATION (LENGTH DIRECTION)	P ₂	$\textbf{2.00} \pm \textbf{0.05}$	$\textbf{0.079} \pm \textbf{0.002}$	



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